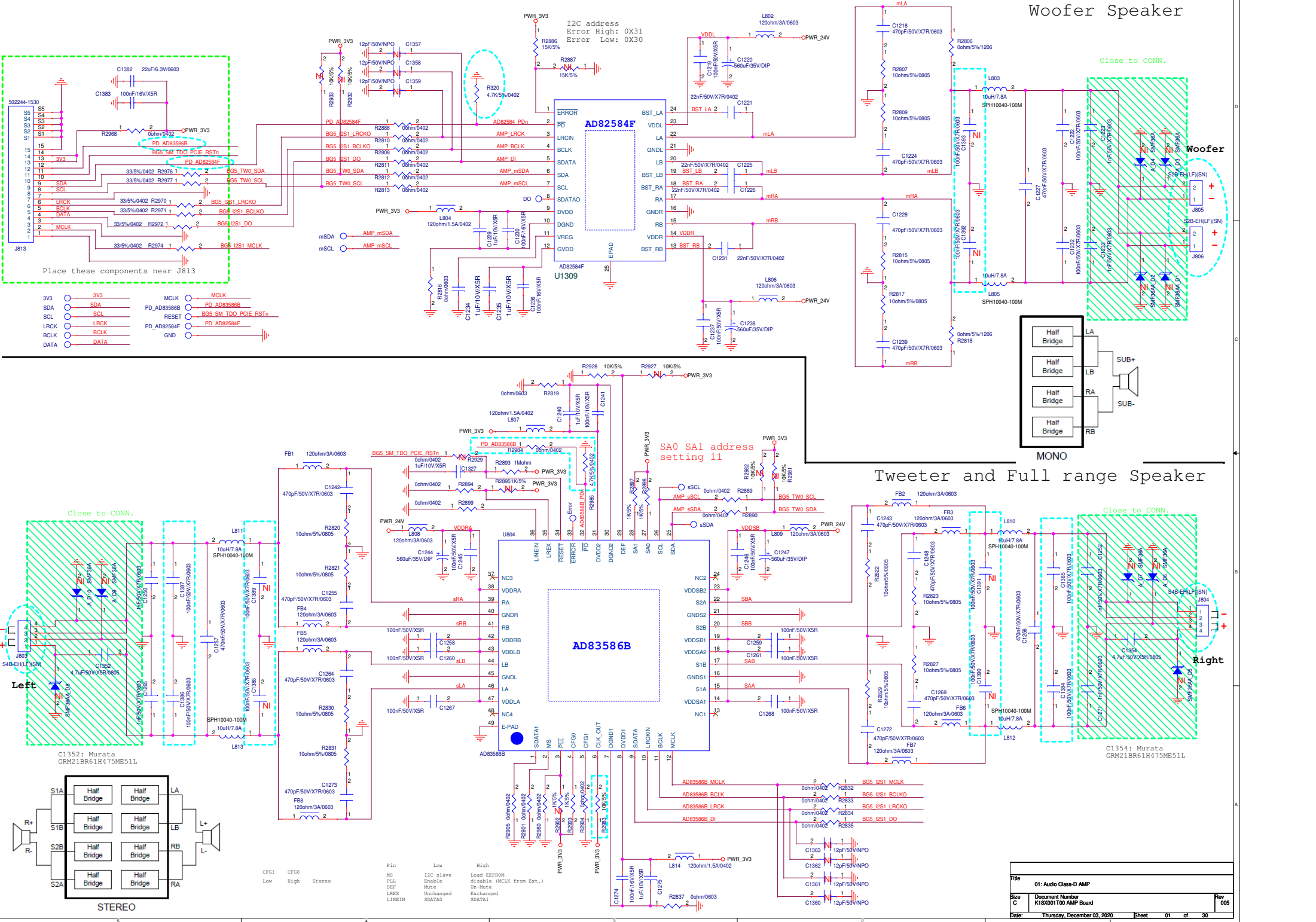
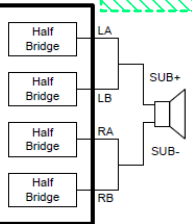




Woofer Speaker

Close to CONN.

Woofer

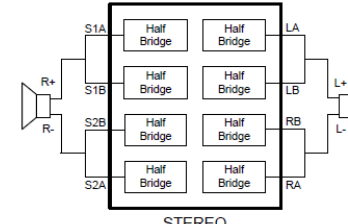


MONO

Tweeter and Full range Speaker

Close to CONN.

Right

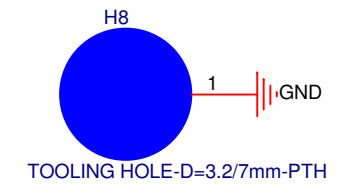
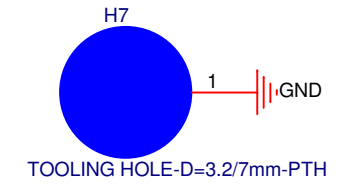
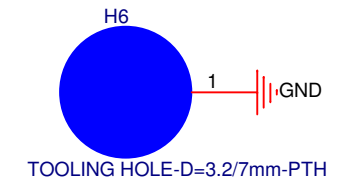
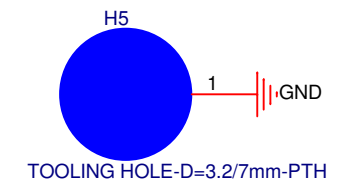
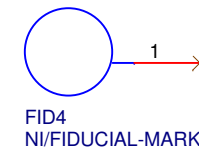
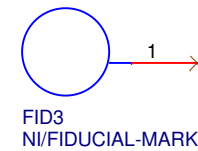
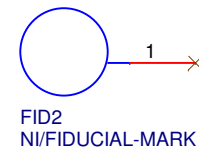
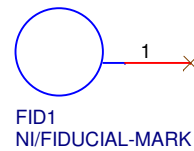
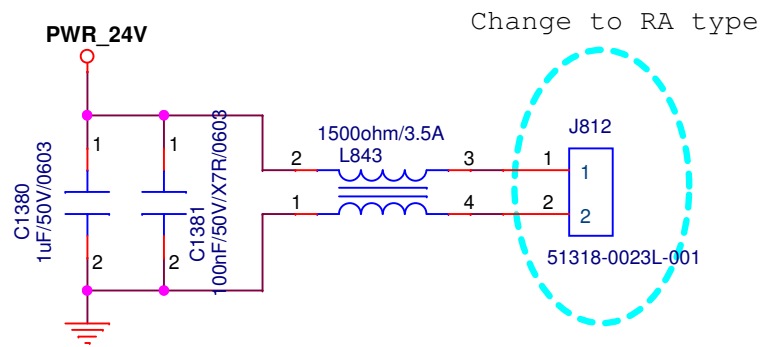


STEREO

Title			
01: Audio Class-D AMP			
Size	Document Number		Rev
C	K18X001T00 AMP Board		005
Date:	Thursday, December 03, 2020	Sheet	01 of 30

Rev#	REVISE HISTORY
V005	The first version, MONO Class-D AMP: AD82584F; Stereo Class-D AMP: AD83586B
V015	- AD82584F PD pin: add 4.7K pull-low resistor, to keep logic low during kernal panic. to avoid DC24V burn speaker. - AD83586B PD pin: add 4.7K pull-low resistor, keep logic low during kernal panic, to avoid DC24V burn speaker. - AD83586B CLK_OUT pin: add 10K pull-high resistor, set internal PLL off. Because MCLK is 24.576MHz, to avoid output high frequency noise. - Separate PD control of the two AMP IC from one GPIO to two GPIO. For software easy initial the two AMP IC. - Changes MID. range and Tweeter Speaker connector from ST type to RA type, for easy assembly. - Changes MONO SPK CONN. to two 1x2pin RA type CONN. (J805, J806) - Changes polarity of J803, pin1/2 polarity +; pin3/4 polarity - . - Changes 2pin Power CONN. (J812) to RA type for easy assembly. - Add 100nF capacitor at location C1384~C1387 for EMI. - R2976, R2977, R2970, R2971, R2972, R2974: 0 ohm/0402 ->33 ohm/0402 - Reserve C1388~C1393 for EMI. - Reserve A_D1~A_D10 for ESD. - Delete R2967,R2969,R2973,R2978,R2979. For ESD good gnd contact. - Delete L839~L842 (Bead), and add L843 (Power common choke) for RS test.

Title		
03: Revise History		
Size A	Document Number K18X001T00 AMP Board	Rev 005
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Title		
02: Screw & Tooling Hole		
Size A	Document Number K18X001T00 AMP Board	Rev 005
Date:	Thursday, December 03, 2020	Sheet 02 of 02